FSA4157 Low Voltage 1 Ω SPDT Analog Switch

FAIRCHILD

SEMICONDUCTOR

FSA4157 Low Voltage 1 Ω SPDT Analog Switch

General Description

FSA4157 is a high performance Single Pole/Double Throw (SPDT) analog switch. The device features ultra low R_{ON} of 1.15 Ω maximum at 4.5V V_{CC} and will operate over the wide V_{CC} range of 1.65V to 5.5V. The device is fabricated with sub-micron CMOS technology to achieve fast switching speeds and is designed for break-before-make operation. The select input is TTL level compatible.

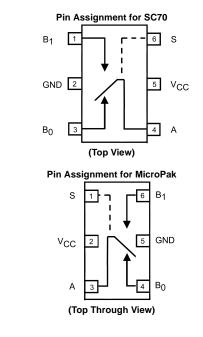
Features

- Smallest low ohmic analog switch
- \blacksquare Maximum 1.15 Ω On Resistance (R_{ON}) at 4.5V V_{CC}
- $\blacksquare 0.3 \ \Omega \text{ max } \mathsf{R}_{\mathsf{ON}} \text{ flatness at 4.5V } \mathsf{V}_{\mathsf{CC}}$
- Space saving MicroPak[™] and SC70 6-lead surface mount packages
- Broad V_{CC} operating range: 1.65V to 5.5V
- Fast turn-on and turn-off time
- Break-before-make enable circuitry
- Over-voltage tolerant TTL compatible control circuitry

Ordering Code:

Order Number	Package Number	Product Code Top Mark	Package Description	Supplied As
FSA4157P6	MAA06A	A57	6-Lead SC70, EIAJ SC88, 1.25mm Wide	250 Units on Tape and Reel
FSA4157P6X	MAA06A	A57	6-Lead SC70, EIAJ SC88, 1.25mm Wide	3k Units on Tape and Reel
FSA4157L6X	MAC06A	EG	6-Lead MicroPak, 1.0mm Wide	5k Units on Tape and Reel

Analog Symbols



Truth Table

Control Input (S)	Function			
L	B ₀ Connected to A			
Н	B ₁ Connected to A			

H = HIGH Logic Level L = LOW Logic Level

Pin Descriptions

Pin Name	Description
A, B ₀ , B ₁	Data Ports
S	Control Input

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Absolute Maximum Ratings(Note 1)

Absolute Maximum Rat	tings(Note 1)	Recommended Operating				
Supply Voltage (V _{CC})	-0.5V to +6.0V	Conditions (Note 3)				
DC Switch Voltage (Note 2)	–0.5V to V _{CC} +0.5V	Supply Voltage (V _{CC})	1.65V to 5.5V			
DC Input Voltage (VIN) (Note 2)	-0.5V to +6.0V	Control Input Voltage	0V to V _{CC}			
DC Input Diode Current	–50 mA	Switch Input Voltage	0V to V _{CC}			
Switch Current	200 mA	Operating Temperature	$-40^{\circ}C$ to $85^{\circ}C$			
Peak Switch Current		Thermal Resistance θ_{JA} in still air				
(Pulse at 1 mS duration,		SC70 6L Package	350°C/W			
<10% Duty Cycle)	400 mA	MicroPak 6L Package	330°C/W (estimated)			
Power Dissipation (P _D) @ 85°C						
SC70 6L Package	180 mW					
MicroPak 6L Package	180 mW					
Storage Temperature Range (T _{STG})	$-65^{\circ}C$ to $+150^{\circ}C$	Note 1: The "Absolute Maximum Ratings" are those values beyond which				
Maximum Junction Temperature (T_J)	+150°C	the safety of the device cannot be guarar operated at these limits. The parametric				
Lead Temperature (T _L)		Characteristics tables are not guaranteed at the absolute maximum rating. The "Recommended Operating Conditions" table will define the conditions				
(Soldering, 10 seconds)	+260°C	for actual device operation.				
ESD (Human Body Model)	7500V	Note 2: The input and output negative ratir and output diode current ratings are observed				
		Note 3: Control input must be held HIGH of	or LOW and it must not float.			

DC Electrical Characteristics (all typical values are at 25°C unless otherwise specified)

Symbol	Parameter	V _{CC}	T _A = +25 °C		$T_A = -40^{\circ}C$ to $+85^{\circ}C$		Units	Conditions	
		(V)	Min	Тур	Max	Min	Max	Units	Conditions
VIH	Input Voltage High	2.7 to 3.6				2.0		v	
		4.5 to 5.5				2.4		v	
V _{IL} Input V	Input Voltage Low	2.7 to 3.6					0.6	v	
		4.5 to 5.5					0.8	v	
I _{IN} Control Input Le	Control Input Leakage	2.7 to 3.6				-1.0	1.0	uА	
		4.5 to 5.5				-1.0	1.0	μА	$V_{IN} = 0V \text{ to } V_{CC}$
I _{NO(OFF)} ,	OFF Leakage Current of		0.0			-20.0	20.0	nA	A = 1V, 4.5V
I _{NC(OFF)}	Port B ₀ and B ₁	5.5	-2.0		2.0				$B_0 \text{ or } B_1 = 4.5 \text{V}, 1 \text{V}$
I _{A(ON)}	ON Leakage Current of	5.5	-4.0	4.0	-40.0	40.0	nA	A = 1V, 4.5V	
	Port A				4.0	-40.0	40.0	IIA	B_0 or $B_1 = 1V$, 4.5V or Floating
R _{ON}	Switch ON Resistance	2.7		2.6	4.0		4.3	Ω	$I_{OUT} = 100$ mA, B_0 or $B_1 = 1.5$ V
	(Note 4)	4.5		0.95	1.15		1.3	52	$I_{OUT} = 100$ mA, B_0 or $B_1 = 3.5$ V
ΔR_{ON}	On Resistance Matching Between Channels (Note 5)	4.5		0.06	0.12		0.15	Ω	$I_{OUT} = 100 \text{mA}, B_0 \text{ or } B_1 = 1.5 \text{V}$
R _{FLAT(ON)}	On Resistance Flatness	2.7		1.4					I _{QUT} = 100mA,
(,	(Note 6)							Ω	$B_0 \text{ or } B_1 = 0V, 0.75V, 1.5V$
		4.5		0.2	0.3		0.4		$I_{OUT} = 100$ mA, B_0 or $B_1 = 0$ V, 1V, 2V
I _{CC}	Quiescent Supply Current	3.6		0.1	0.5		1.0		$V_{IN} = 0V \text{ or } V_{CC}, I_{OUT} = 0V$
		5.5		0.1	0.5		1.0	μA	

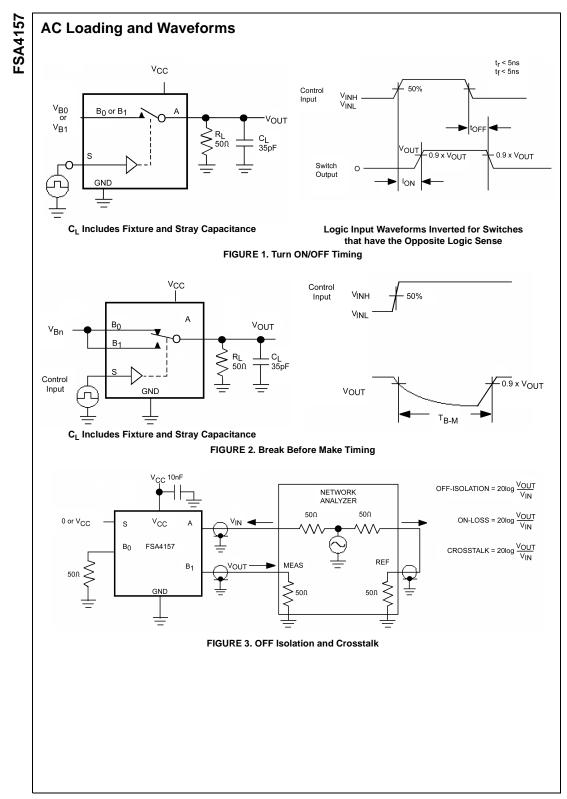
Note 4: Measured by the voltage drop between A and B pins at the indicated current through the switch. On Resistance is determined by the lower of the voltage on the two (A or B Ports).

Note 5: $\Delta R_{ON} = R_{ON max} - R_{ON min}$ measured at identical V_{CC}, temperature and voltage.

Note 6: Flatness is defined as the difference between the maximum and minimum value of On Resistance over the specified range of conditions.

DN Turn	arameter	Vcc	T _A = +25 °C T			$I_{A} = -40^{\circ}C$	$T_A = -40^{\circ}C \text{ to } +85^{\circ}C$			6	onditions	Figure
_{DFF} Turn			Min	Тур	Мах	Min	Max	Units		00	hallons	Numbe
011	ON Turn ON Time				50.0		60.0	ns			, $R_L = 50\Omega$, $C_L = 35 \text{ pF}$	Figure 1
011		4.5 to 5.5 2.7 to 3.6			35.0		40.0		-		$R_L = 50\Omega, C_L = 35 \text{ pF}$	r igure r
Time	OFF Turn OFF				20.0		30.0	ns			$R_L = 50\Omega, C_L = 35 \text{ pF}$	Figure 1
-	Time M Break Before Make Time				15.0		20.0		B ₀ o	r B ₁ = 3V, F	$R_L = 50\Omega, C_L = 35 \text{ pF}$	
								ns	ns			Figure 2
		4.5 to 5.5	<u> </u>	20.0							01/	-
Charge		2.7 to 3.6		10.0				рС	$C = 1.0 \text{nF}, V_{GE}$		= 0V,	Figure
Injection		4.5 to 5.5		20.0					$R_{GEN} = 0\Omega$			
OIRR OFF-	- Isolation	2.7 to 3.6		-70.0				dB	f = 1	MHz, R _L =	50Ω	Figure
(tall)	a a t a ll r	4.5 to 5.5		-70.0								
Ktalk Cros	sstalk	2.7 to 3.6	<u> </u>	-70.0				dB	f = 1	MHz, R _L =	50Ω	Figure 3
2/0/	h Bandwidth	4.5 to 5.5	<u> </u>	-70.0								
3W –3db	b Bandwidth	2.7 to 3.6 4.5 to 5.5	<u> </u>	350		├		MHz	$R_L =$	50Ω		Figure 6
HD Tota	al Harmonic	4.5 to 5.5 2.7 to 3.6	<u> </u>	350 0.002		├───			R	6000 V	= 0.5V P.P,	
	tortion							%		000 <u>2</u> , v _{IN} 0 Hz to 20		Figure
DISIC	lonion	4.5 to 5.5		0.002				ļ	1 = 2	U HZ 10 20	K HZ	
Capacitance			V _{cc}		T _A = +25°	<u>.</u>	T _A = 40°	C to +	85°C	1		Figure
Symbol	Parameter		(V)	Min	Тур	Max	Min		ax	Units Cor	Conditions	Numbe
IN Con	ntrol Pin Input		(•)		Typ	Max			an		f = 1MHz	Numbe
	pacitance		0		3.5					pF		Figure
	Port OFF					-					f = 1MHz	
	pacitance		4.5		12.0					pF		Figure
						+				-	f = 1MHz	
	•		4.5		55.0					pF		Figure
C _{ON} On Capacitance			4.5		55.0					pF		Figure

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